Simple Diff. Input OTA
Folded-Cascode Schematic

Folded-Cascode Noise

\[ nf = \]
Simulation Schematic

Many more transistors, but overall characteristics very similar to common-source stage.

Bias, V^*

[Graphs showing bias voltage and current characteristics]
Low Frequency Gain, $A_{dm}$

Output Resistance

Beware of $r_o$ imbalance between NMOS and PMOS current sources
Input Cascode

Biasing and Parasitic Feedback