Design Example

• What are the specs?
  • And where do they come from?

• Example: 50MS/s, ~11-bit ADC
  • Accuracy:
  • Settling time:
  • Dynamic range:
  • Closed-loop gain:
  • Power:

Design Procedure

Finding $C_L$

Settling
Device Sizing

Single-ended vs. Differential

Settling?

Slewing