flowchart is similar to that given in Fig.3.

6. CONCLUSIONS

The data signal generation from stored elements avoids multiplications and permit a high processing speed.

The memory size is small, with a proper choice of $f_p/f_o$ ratio. Hence, the method is useful in implementation of models.

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A UNIFIED THEORY FOR THE COMPUTER AIDED ANALYSIS OF GENERAL SWITCHED CAPACITOR NETWORKS

ABSTRACT

We derive a general theory for the analysis of switched capacitor circuits. The circuits can have 2 phases or more and have no topologic constraints whatsoever. Also the inputs are arbitrary. We describe efficient computations of the time domain response, frequency response, noise and sensitivity analysis. All derivations are carried out in the modified modal analysis framework, which allows an easy implementation in a computer program. The theory is simple and applies all previously described analysis techniques.

1. INTRODUCTION

Switched capacitor (SC) circuits have received much attention in recent years, because they all allow to realize a filter with low sensitivity as an integrated circuit. Since these circuits are usually large, the analysis should be done by computer. Two types of computer implementations have been described [1-5], one based on time-domain analysis and one based on s-domain analysis. Here we present a unifying framework for both, which then also allows to evaluate their relative merits.

2. THEORETICAL ASPECTS OF THE ANALYSIS OF SC-NETWORKS

We consider arbitrary linear networks containing ideal switches, capacitors, independent voltage and charge sources and dependent sources WVS, QQS, ... The switches are controlled by Boolean clock variables $\phi(t) = 0$ or $1$. $\phi(t) = 0$ (resp., $\phi(t) = 1$) corresponds to an open (resp., closed) switch at time $t$ if this switch is driven by clock $r$. The time is partitioned into time slots $t_k = (t_k,t_{k+1})$ such that the clock signals do not vary in $t_k$, i.e. $\phi(t) = \phi(t_k)$ for $t \in t_k$. We assume that the clock signals are $T$-periodic, with $N$ time slots (called $N$ phases) in one period of duration $T$.

**Theorem 1**: If any above defined SC network is excited by a piecewise-constant excitation $y(t) = \psi_n$, $\zeta(t) = \psi_n$, $t \in t_k$, then its response in the
time domain is also piecewise-constant \( v(t) = v_m, \quad q(t) = q_m \) in \( \Delta_m \) and given by

\[
\begin{bmatrix}
\Delta_k & B_k \\
C_k & D_k
\end{bmatrix}
\begin{bmatrix}
\begin{bmatrix} v_{k+\Delta N} \\ v_{k+\Delta N-1} \end{bmatrix} \\
0
\end{bmatrix} =
\begin{bmatrix}
\begin{bmatrix} v_k \\ v_{k-1} \end{bmatrix} \\
0
\end{bmatrix} +
\begin{bmatrix}
\begin{bmatrix} q_k \\ q_{k-1} \end{bmatrix} \\
0
\end{bmatrix}
\]

(1)

where \( v(t) \) (resp., \( y(t) \)) is the vector of the voltage responses at the nodes (resp., voltage sources in some selected branches) and where \( q(t) \) (resp., \( y(t) \)) is the vector of the charges transferred in some selected branches (resp., injected by charge sources in the nodes) between \( t_{k+\Delta N} \) and \( t \) for \( t \in \Delta_{k+\Delta N} \), and where the contributions to \( A_k, B_k, C_k \) and \( D_k \) are given by the stamps of the classical modified nodal analysis and where the contribution of a switch \( S \) connecting node \( i \) to \( j \) and governed by clock \( \delta_{ij}^S \), is given by

\[
\begin{bmatrix}
0 & 0 & 1 & 0 \\
0 & 0 & 0 & 1 \\
\delta_{ij}^S & \delta_{ij}^S & 0 & 0 \\
-\delta_{ij}^S & -\delta_{ij}^S & 0 & 0
\end{bmatrix}
\begin{bmatrix}
\begin{bmatrix} v_{1k} \\ v_{jk} \\ v_{1k} \\ v_{jk} \end{bmatrix} \\
0
\end{bmatrix} =
\begin{bmatrix}
0 \\
0
\end{bmatrix}
\]

(2)

where \(-\) denotes the complement of a Boolean variable.

If the SC network is excited by an arbitrary excitation \( v(t), y(t) \), then the piecewise-constant part of the input and output \( v_m = y(t_{m+1}),\ y_m = y(t_{m+1}) \) and \( q_m = y(t_{m+1}) \) are determined by (1) and the remainder waveforms \( v^*(t) = v(t)-v_m, \ y^*(t) = y(t)-y_m, \ v^*(t) = y(t)-y_m \) and \( q^*(t) = q(t)-q_m \) for \( t \in \Delta_m \) are related by

\[
\begin{bmatrix}
\Delta_k & B_k \\
C_k & D_k
\end{bmatrix}
\begin{bmatrix}
\begin{bmatrix} v^*(t) \\ q^*(t) \end{bmatrix}
\end{bmatrix} =
\begin{bmatrix}
\begin{bmatrix} v^*(t) \\ q^*(t) \end{bmatrix}
\end{bmatrix}
\]

(3)

Proof: The first set of equations of (1,3) express the charge conservation and the second set are the constitutive relations of some selected branches.

Theorem 2: Given any above defined SC network with a piecewise-constant excitation. Define

\[
v_m(z) = \sum_{k=0}^{\infty} \begin{bmatrix} v_{m+\Delta N} \\ v_{m+\Delta N} \end{bmatrix} z^{-k}
\]

(4)

and analogous equations for \( y_m, q_m, \) and \( \omega_m \) where \( m = 1, 2, \ldots N \). Then these variables are related by

\[
A_1 \cdot A_2 \cdot A_3 \cdot \ldots \cdot A_N
\]

(5)

where the missing entries are zero, and the submatrices are defined in Theorem 1.

Proof: Apply the techniques of z-transform and (4) on (1).

Corollary 1: Any above defined SC network is completely characterized by the z-domain transfer matrix \( Y \) of

\[
\begin{bmatrix}
\begin{bmatrix} A_{11} & \ldots & A_{1N} \\ \vdots & \ddots & \vdots \\ A_{N1} & \ldots & A_{NN} \end{bmatrix}
\end{bmatrix}
\quad \begin{bmatrix}
\begin{bmatrix} H_{11} & \ldots & H_{1N} \\ \vdots & \ddots & \vdots \\ H_{N1} & \ldots & H_{NN} \end{bmatrix}
\end{bmatrix}
\begin{bmatrix}
\begin{bmatrix} y_1 \\ \vdots \\ y_N \end{bmatrix}
\end{bmatrix}
\end{bmatrix}
\begin{bmatrix}
\begin{bmatrix} y_1 \\ \vdots \\ y_N \end{bmatrix}
\end{bmatrix}
\]

(6)

Proof: Eq. (6) is obtained by inverting the matrix in (5). Eq. (3) can easily be recovered from \( H \) at \( z = 0 \).

The submatrices \( A_{k,k}, H_{k,k}, K_{k,k} \) and \( L_{k,k} \) have a very simple interpretation. If only nonzero voltage sources are supplied in time slots \( k, k+N, k+2N, \ldots \) and if the output node voltages are only observed at the end of time slots \( k, k+N, k+2N, \ldots \) then \( H_{k,k}(z) \) relates inputs at phase \( k \) to outputs at phase \( k' \) i.e.
It is shown in [6] that for any SC network \( \mathcal{N} \) and adjoint SC network \( \mathcal{N}^\prime \) can be derived with a z-domain transfer matrix \( \mathbf{H} \), whose columns correspond with rows of \( \mathbf{H}^\prime \).

3. COMPUTATION OF FREQUENCY, NOISE AND SENSITIVITY PROPERTIES OF SC NETWORKS

Since a SC network is a time-varying circuit, a sinusoidal excitation may generate many frequencies in the output. However only one is of interest in the following practical frequency domain transfer function \( \mathcal{H}(\omega) \) [1,3], which relates by definition the phasor \( \mathbf{U} \) of the sinusoidal excitation \( \mathbf{y}(t) = \mathbf{U} e^{j \omega t} \) to the phasor at the same pulsation \( \omega \) in the output \( \mathbf{y}(t) \). Standard Fourier transform techniques allow to prove that

\[
\mathcal{H}(\omega) = \sum_{k=1}^{N} \mathbf{h}_k(\omega) = \sum_{k=1}^{N} \mathbf{e}^{j \omega t_k} \mathbf{v}_k(\omega) \mathbf{e}^{j \omega t_k} + \left( \mathbf{t}_k - \mathbf{t}_k^\prime \right) \mathbf{e}^{j \omega t_k} \mathbf{v}_k(\omega) \mathbf{H}_{kk}(\omega),
\]

where

\[
\mathbf{v}_k(\omega) = 2 \sin(\pi t_k/2) \mathbf{v}(\omega) \mathbf{e}^{j \omega t_k}/2, \quad \mathbf{v}_k(\omega) = \mathbf{v}(\omega) \mathbf{e}^{j \omega t_k}/2, \quad \mathbf{v}_k(\omega) = \mathbf{v}(\omega) \mathbf{e}^{j \omega t_k}/2.
\]

Similar expressions can be derived for the aliasing terms. In the computation of one entry \( \delta \mathbf{H}(\omega) \) of (8.a) via the time-domain, \( N \) impulse responses have to be determined using (1) for impulse excitations in the \( N \) phases. The \( \mathbf{h}_k(\omega) \) of (8.a) are then obtained via an FFT. In the z-domain we solve (5) once for \( z = e^{j \omega t_k} \) and \( \mathbf{U}_z = \mathbf{v}_z \), \( \mathbf{U}_z = \mathbf{0} \) for \( n \neq j \) and \( \mathbf{U}_z = \mathbf{e}^{j \omega t_k} \) for \( k = 1, \ldots, N \) in order to obtain the sums in brackets in (8.a).

In noise analysis one is usually interested in the output noise voltage at one node \( i \) due to the noise in many (say \( a \)) components. Let the vector \( \mathbf{v}(t) \) of the input noise voltage sources be characterized by a given spectral density matrix \( \mathbf{S}(\omega) \) which depends on the technology. Then the average spectral density function is given by

\[
\mathbf{S}(\omega) = \sum_{a=0}^{\infty} \mathbf{z}^{(1)}(\omega, a, 2\pi) \mathbf{S}(\omega, a, 2\pi) \mathbf{z}^{(1)}(\omega, a, 2\pi)
\]

where

\[
\mathbf{z}^{(1)}(\omega) = \sum_{a=0}^{\infty} \mathbf{z}^{(1)}(\omega, a, 2\pi) \mathbf{z}^{(1)}(\omega, a, 2\pi)
\]

and \( \mathbf{S} \) denotes complex conjugate and transpose and \( \mathbf{z} \) with \( \mathbf{z} = N-k, k = N-k \) is the corresponding submatrix of the z-domain transfer function of the adjoint SC network and \( \mathbf{H}(\omega) \) (resp., \( \mathbf{H}(\omega) \)) is the i-th row (resp., j-th column) of the matrix \( \mathbf{H} \). In the time domain analysis all required \( \mathbf{z}^{(1)}(\omega) \) can be obtained from \( N \) impulse responses of the adjoint SC network and FFT's. In the z-domain analysis all required sums in brackets in (9.a) are obtained by solving for each frequency one set of equations (5) for the adjoint SC network with an appropriate excitation.

In sensitivity analysis one is interested in the effect of a variation in one or several components or the frequency on the output relation. They can be computed by first deriving the sensitivity of an arbitrary entry \( \mathbf{H}(\omega) \) of the z-domain transfer function (6) and then using these equations to compute the sensitivity of the frequency domain transfer function.

\[
\mathbf{H}(\omega) = \mathbf{A}(\omega) \mathbf{e}^{j \phi(\omega)}
\]

We illustrate this by computing the group delay

\[
\mathbf{S}(\omega) = \mathbf{S}(\omega) \mathbf{e}^{j \phi(\omega)} = \mathbf{S}(\omega) \mathbf{e}^{j \phi(\omega)}
\]

where

\[
\mathbf{S}(\omega) = \mathbf{S}(\omega) \mathbf{e}^{j \phi(\omega)} = \mathbf{S}(\omega) \mathbf{e}^{j \phi(\omega)}
\]

In order to obtain the group delay and all other sensitivities via the time domain, \( N \) impulse responses of the nominal and \( N \) impulse responses of the adjoint SC network and some FFT's have to be computed. In the z-domain ana-
lization, (10.a) is obtained by solving three sets of equations (5) for the nominal SC network and one for the adjoint network.

4. CONCLUSION AND COMPARISON

We have described an elegant and general theory for the analysis of SC circuits from which the computer implementations via time domain or via z-domain are derived. Other analysis techniques [2-5] are either restricted to two-phase SC networks [4-5] or require lengthy derivations [2-3] in order to obtain equations equivalent to (1) or (5). Although [2-3] may have less equations to solve, we are convinced that the use of macromodels in our framework can provide us with similar reductions. Moreover, the search for the minimal set of equations is largely academic, since sparse matrix methods have a computational complexity, which depends on the zero-nonzero pattern and not on the size of the matrix. It is important to observe that the use of the adjoint SC network (6) allowed great computational savings and hence it is equally useful in [2-5]. Let us conclude by comparing the time and z-domain technique. The time domain requires repeated solutions of (1), which is a smaller set of equations than (5), but it requires additional FFT's. The time domain analysis is implemented in DIANA [1] and progress is being made toward an implementation of the z-domain analysis.

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NORATOR-MULLATOR FORMULATION OF THE NETWORK
FUNCTIONS FOR SWITCHED-CAPACITOR CIRCUITS

ABSTRACT: A norator-nullator approach is used to express the z-domain network functions of switched capacitor circuits as a signed ratio of two determinants, i.e. in a form which permits the determination of poles and zeros by means of the well known two-sets-of-eigenvalues approach. The involved nodal capacitance matrices are completely reduced with respect to all constraints imposed by closed switches, operational amplifiers and port-conditions. An algorithm is described, whereby these matrices can be compiled directly from the input circuit description.

1. Introduction

Recently several approaches to computer-aided analysis of switched-capacitor circuits have appeared [4][5][6] and undoubtedly many more will show up. The method described here is aimed at z-domain analysis of sampled-input, two-phase, equal time-slot, switched-capacitor two-ports consisting of capacitors, ideal switches and ideal operational amplifiers. It is based on Kirtih's and Moschytz's fundamental nodal approach [1], and is related to Hekemek's and Moschytz's later computer-oriented method [5], but emphasizes and simplifies the algorithmic aspects in obtaining the necessary matrices. The theory is developed for two-phase switching schemes, but the extension to multiphase systems is obvious.

2. Universal formulas for switched-capacitor network functions

Fig. 1 shows the equivalent z-domain four-port (1) corresponding to a two-phase switched-capacitor two-port with equal time-slots A and B, and defines the port-node designations and sign-conventions adopted in the following. Except for the datum node 0, every node k (k is a non-negative integer) in the physical two-port has an equivalent A-node: k+1 and an equivalent B-node: k-n in the four-port.

Fig. 2 defines the transfer function H(z) = V(z)/U(z) with the generator and meter quantities: V(z) and U(z) being of type V or Z. Proper definition of H(z) requires that input-port B is short-circuited if H(z) is a voltage and that output-port A is short-circuited if H(z) is a charge. This is taken care of by the hypothetical switches SS and SM respectively. With these conditions satisfied H(z) is found by nodal analysis of the equivalent two-port defined by ports p, q and r, s. Since similar definitions apply for N(z), MB and NB we now drop the phase designation for H, p and q.

In order to obtain the reduced nodal z-domain capacitance matrix, let us first remove all closed switches and operational amplifiers from the equivalent two-port. This leaves an all-capacitive (possibly disconnected) structure with a 2n:2n nodal capacitance matrix C(z) of the form shown in Eq. 1:

\[
C(z) = \begin{bmatrix}
\begin{array}{cc}
\frac{z^{1/2}}{z^{1/2}} & (z^{1/2})^{-1} \\
(z^{1/2})^{-1} & \frac{z^{1/2}}{z^{1/2}}
\end{array}
\end{bmatrix}
\begin{bmatrix}
\begin{array}{c}
A \\
B
\end{array}
\end{bmatrix}
\]

where C is the simple real b×b nodal capacitor matrix of the correspondingly stripped physical circuit.

Next, let us model the short-circuits and op-amps by means of nullator-norator pairs as shown in Fig. 3, and reconstruct the equivalent two-port by